



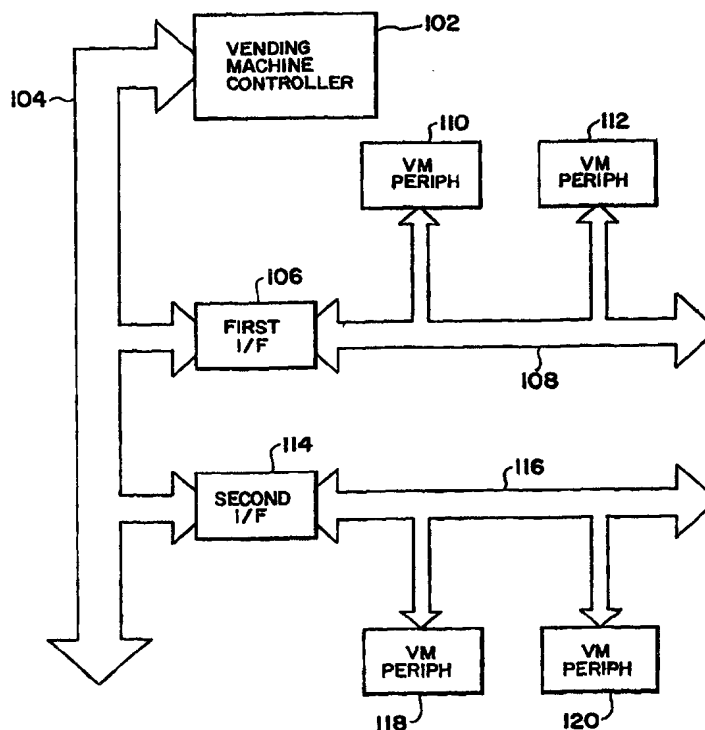
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(54) Title: VENDING MACHINE DUAL BUS ARCHITECTURE

(57) Abstract

Disclosed is a vending machine operating system architecture that connects vending machine peripherals (110, 112, 118, 120, 216, 218, 222, 224) of different communication standards to a main vending machine controller. One way to connect the main controller (102) to disparate buses (108, 114) is to use a UART device (106, 114) for each. An even more cost effective approach is to use a communications co-processor (202) (which is at least equivalent to a UART device) to which each of the two disparate buses (214, 220) is selectively connected by a multiplexer (208) under the control of the main vending machine controller (200).



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VENDING MACHINE DUAL BUS ARCHITECTURE

TECHNICAL FIELD

The invention is directed toward a vending machine operating system architecture, and more particularly to a vending machine operating system architecture for operatively connecting vending machine peripherals of
5 different communication standards to a main controller.

BACKGROUND ART

Vending machines have not been standardized throughout the world. Rather, vending machines are as varied as the cultures of the countries in which the machines are located. For example, vending machines in Japan
10 are very different than vending machines in the United States.

The Japanese culture is accustomed to buying a great many goods from a great variety of relatively sophisticated vending machines. The United States, in contrast, has a culture which is accustomed to purchasing a much smaller variety of goods from generally less sophisticated vending machines.
15 The Japanese market tends to produce more sophisticated and more expensive hardware/architecture from which its vending machines are constructed. In contrast, the hardware of U.S. vending machines tends to be less sophisticated and less expensive.

At a basic level, vending machines throughout the world share similar
20 attributes. For example, they all include a controller that controls the operation of numerous peripheral devices. These peripherals are connected to the controller via a communications bus. In the United States, the standard vending machine communications bus is the MDB standard, i.e., the "International Multi-drop Interface Standard," established by the National
25 Automatic Merchandising Association (NAMA) of Chicago, Illinois. As

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expected, this is not the vending machine communication standard in Japan. There, the standard appears to be the VCCS standard.

5 A universal characteristic of the market for machine-vended goods is that these markets are extremely sensitive to the hardware costs of the vending machine. A small difference in cost of the hardware for a given machine can greatly effect its profitability.

To use a Japanese bill and coin validation mechanism, for example, a U.S. machine requires the Japanese mechanism to be modified so that it can communicate using the MDB protocol rather than the VCCS protocol.
10 Alternatively, a specialized adapter could be constructed to convert from the VCCS protocol to the MDB protocol so that the Japanese mechanism could be used without modification. Both of these techniques significantly raise the cost of using the Japanese mechanism or peripheral.

Another option for using a Japanese peripheral device in a U.S. machine would be to provide the U.S. machine with a controller having two serial ports. One of the ports would be dedicated to the MDB bus, while the other would be dedicated to the VCCS bus. Unfortunately, this requires a custom integrated circuit (IC) that has two serial ports. Such a customized IC is very expensive.

20 It is a problem to integrate vending machine peripheral devices of disparate communication standards, e.g., MDB and VCCS, into a single vending machine and yet still have acceptably low hardware costs given the extreme price sensitivity of the market for machine-vended goods.

DISCLOSURE OF INVENTION

25 It is an object of the invention to solve the problems of the conventional art discussed above.

It is an object of the invention to provide a vending machine architecture for operatively connecting vending machine peripherals of

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different communication standards to a main controller in a cost effective manner given the extreme cost sensitivity in the market for machine-vended goods.

The objects of the invention are fulfilled by providing a vending machine operating system architecture for operatively connecting vending machine peripherals of different communication standards to a main controller, the architecture comprising: a main controller; a first bus for connecting to at least one vending machine peripheral according to a first communication standard; a second bus for connecting to at least one vending machine peripheral according to a second communication standard; and connection means for connecting at least one of said first bus and second bus to said main controller.

The connection means of the present invention can be embodied by two universally synchronous receiver/transmitter (UART) devices for the first and second buses, respectively. Alternatively and preferably, the connection means can be embodied by a multiplexer for selectively connecting the main controller to either the first bus or the second bus.

The foregoing and other objectives of the present invention will become more apparent from the detailed description given hereinafter. However, it should be understood that the detailed description and specific examples, while indicating preferred embodiments of the invention, are given by way of illustration only, since various changes and modifications within the spirit and scope of the invention will become apparent to those skilled in the art from this detailed description.

BRIEF DESCRIPTION OF DRAWINGS

Fig. 1 is a block diagram of the first embodiment of the invention;

Fig. 2 is a block diagram of a second embodiment of the invention; and

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Fig. 3 depicts the look-up table of the second embodiment in more detail.

MODES FOR CARRYING OUT THE INVENTION

5

Fig. 1 is block diagram of the first embodiment of the present invention.

10 In Fig. 1, a vending machine controller 102 is connected to a main, preferably parallel, bus 104. A first interface 106 is connected between the main bus 104 and a first bus 108. The first bus 108 supports communication according to a first communication standard. Peripheral vending machine devices 110 and 112 are connected to the first bus 108 and communicate via the first communication standard. Examples of the peripherals 110 and 112 include coin mechanisms, credit card readers and bill validators. The peripherals 110 and 112, and the first bus 108, preferably
15 conform to a serial standard, and more preferably conform to the MDB communication standard.

Also in Fig. 1, a second interface 114 connects the main bus 104 to a second bus 116. The second bus 116 supports communications according to a second communication protocol different than the protocol for the first bus
20 108. Vending machine peripheral devices 118 and 120 are connected to the second bus 116. Examples of the peripherals 118 and 120 include coin mechanisms, credit card readers and bill validators.

The communications standard supported by the second bus 118 and to which the peripherals 118 and 120 conform is preferably a serial standard, and is more preferably the VCCS standard of Japan. Alternatively, the
25 protocol supported by the second bus 116 and the peripherals 118 and 120 could be any other well known serial standard such as RS232, RS 484 or the IRDA (for infrared data transfer).

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It is noted that only two (2) peripheral devices have been depicted for each of the buses 108 and 116 to reduce the complexity of the drawing. These are merely representative of the many peripherals that can be attached to such buses.

5 The interfaces 106 and 114 of Fig. 1 are preferably a universal asynchronous receiver/transmitter (UART) device. Alternatively, the interfaces 106 and 114 could be formed from an array of discreet logic components.

10 The general VCCS electrical specification is: serial, eight data bits, seven wires (two data, sync, signal common, power ground 24v, 8v); 4800 bits per second, 0-24 volts signal. The electrical specification for the MDB protocol is: serial current loop; five wires (two data, signal common, power ground, 34v) nine bits (eight data bits plus 'status right single' bit), 9600 bits per second, 0-5 volts signal (TTL level).

15 Both the MDB and VCCS standards use device addresses from 0 to 31. The VCCS protocol augments the eight data bits with a separate directional (controller to peripheral) synchronization signal line. In contrast, the MDB protocol uses eight data bits plus a bi-directional 'status right single' bit.

Fig. 2 depicts a block diagram of a second embodiment of the invention.

20 In Fig. 2, a main controller 200 includes a main processor 204 and a main communications co-processor 202 connected to the main processor 204 via a preferably parallel bus 206. The communications co-processor 202 has at least the functional capabilities of a UART device. The main processor 204 includes a look-up table 205. The main controller 200, i.e., the
25 communications co-processor 202 is connected to a multiplexer 208 via a bus 210. In addition, the communications co-processor 202 sends one or more control signals to the multiplexer 208 via the bus 212.

The multiplexer 208 of Fig. 2 is connected to a first bus 214 and a second bus 220. The first bus 214 supports a communication protocol

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different than the second bus 220. Vending machine peripheral devices 216 and 218 are connected to the first bus 214 and conform to the communication protocol of the first bus 214. Similarly, vending machine peripheral devices 222 and 224 connect to the second bus 220 and conform to the communication protocol thereof. As with the first embodiment, only two peripheral devices have been depicted as being connected to the buses to simplify the drawings; these are two of but many peripherals that can be connected to the first and second buses, respectively. An additional signal line 226 is shown as conveying a signal from the communications co-processor 202 to each of the peripherals 222 and 224.

The protocol of the first bus is preferably serial and more preferably is the MDB standard. The protocol of the second bus is preferably serial, and is more preferably the VCCS standard of Japan. Alternatively, the protocol of the second bus could be any other well known serial standard such as RS 232, RS 484 or IRDS (for infrared data transfer).

Because the first bus 214 and the second bus 220 are both serial, the bus 210 is necessarily serial. To account for the differences between the MDB standard and the VCCS standard, the additional signal line 226 is provided over which the communications co-processor sends a synchronization signal.

Fig. 3 depicts the look-up table 205 of Fig. 2 in more detail. Column 302 of table 205 lists the identity of a device that is connected to either the first bus 214 or the second bus 220. The second column 204 identifies the bus to which the device identified in the same row at column 302 is connected. The third column 306 lists the address of the device identified in the same row at column 302. Thus, the rows 308, 310, 312 and 314 present the pertinent information for the peripheral devices 216, 218, 222 and 224, respectively.

The operation of the embodiments of the invention will be described below.

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Both of the preferred bus communication protocols, namely the MDB standard and the VCCS standard, use a half-duplex polling scheme rather than an interrupt-driven scheme. The main controller 102 of Fig. 1 or the primary processor 204 of Fig. 2 sequentially polls the various peripherals on the first and second buses 108 and 116, respectively. The peripherals remain silent on the bus until they are polled, at which time they perform an action an/or respond with status information.

Among the components of a vending machine, the UART devices in the interfaces 106 and 114 are comparable, and second only, in cost to the processor embodying the main controller 102. While more cost effective than, e.g., re-engineering a VCCS-based peripheral to operate directly according to the MDB protocol or providing a custom designed converter for converting from the VCCS protocol to MDB protocol, the embodiment of Fig. 1 is more expensive than the embodiment of Fig. 2. Hence the embodiment of Fig. 2 is preferred.

In Fig. 2, one interface in the form of the communications co-processor 202 (having at least the functional capabilities of a single UART), is needed rather than two UART devices. But to achieve this approximately 50% reduction in the number of UART devices required, the multiplexer 208 must be provided. When the main processor 204 desires to poll a peripheral on a given bus, it sends one ore more control signals to the multiplexer 208 via the communications co-processor 202 that causes the multiplexer to connect the bus 210 to the selected first or second bus 214 or 220.

The main processor 204 uses the information within the look-up table 205 to locate a desired peripheral device. For example, if the main processor 204 needs to access the peripheral 216, then it retrieves the identity of the bus to which the peripheral 216 is connected and the address that the peripheral 216 has on that bus by applying the identity of the peripheral 216 to the table 205.

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An advantage of the invention is that it permits peripheral devices conforming to disparate communication protocols to be used within the same vending machine in a cost effective manner despite the extreme cost sensitivity of the market for machine-vended goods. Moreover, the second
5 embodiment only requires the use of a single communications co-processor rather than two UART devices, which represents an approximately 50% cost savings for what is the second most expensive component in a vending machine.

The invention being thus described, it will be obvious that the same
10 may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to one skilled in the art are intended to be included within the scope of the following claims.

What is claimed:

1 1. A vending machine operating system architecture for operatively
2 connecting vending machine peripherals (110, 112, 118, 120, 216, 218, 222,
3 224) of different communication standards to a main controller (102, 200), the
4 architecture comprising:

5 a main controller (102, 200);

6 a first bus (104, 214) for connecting to at least one vending machine
7 peripheral (110, 112, 216, 218) according to a first communication standard;

8 a second bus (116, 220) for connecting to at least one vending machine
9 peripheral (118, 120, 222, 224) according to a second communication
10 standard; and

11 connection means (106, 108, 208, 210, 212) for connecting at least one
12 of said first bus and second bus to said main controller (200).

1 2. The architecture of claim 1, wherein said connection means
2 includes a multiplexer (208) for selectively connecting said main controller
3 (200) to one of said first bus (214) and said second bus (220).

1 3. The architecture of claim 1, wherein said first communication
2 standard and said second communication standard are serial.

1 4. The architecture of claim 3, wherein said first communication
2 standard is the multi-drop bus (MDB) standard.

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1 5. The architecture of claim 3, wherein at least one additional signal
2 line (226) is connected between each of said at least one vending machine
3 peripheral (222, 224) coupled to said second bus (220) and said main
4 controller (200).

1 6. The architecture of claim 3, wherein said second communication
2 standard is the VCCS standard.

1 7. The architecture of claim 1, wherein said main controller
2 includes:

3 a communications co-processor (202) connected to said connection
4 means; and

5 a primary processor (204) connected to said communications co-
6 processor.

1 8. The architecture of claim 7, wherein said main processor
2 includes a look-up table (205) for storing data relating an identity of each of
3 said vending machine peripherals (216, 218, 222, 224) , the buses (214,
4 220) to which said peripherals (216, 218, 222, 224) connect, and the
5 address assigned to said peripherals (216, 218, 222, 224), respectively.

1 9. The architecture of claim 1, wherein said main controller includes
2 a main bus (104) connected to said main controller (102), and said connection
3 means includes a first interface (106) for interfacing said first bus (108) to
4 said main bus (104), and a second interface (114) for interfacing said second
5 bus (116) to said main bus (104).

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- 1 10. The architecture of claim 9, wherein said first interface (106)
2 includes a first universal asynchronous receiver/transmitter (UART) device
3 (106) and said second interface (114) includes a second UART device (114).